

FIG.2A

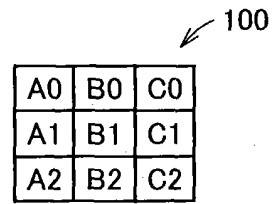


FIG.2B

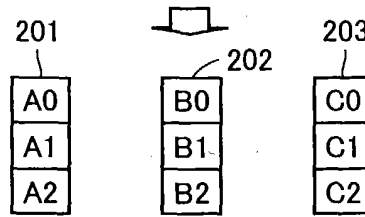


FIG.2C

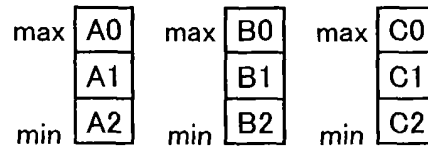


FIG.2D

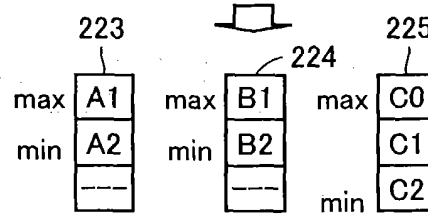


FIG.2E

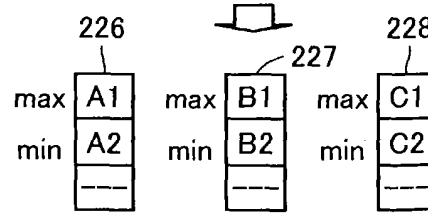


FIG.2F

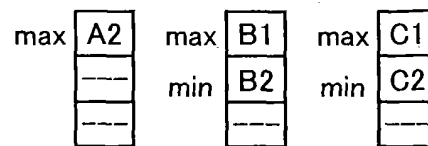


FIG.2G

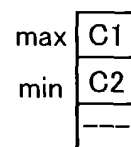


FIG.3

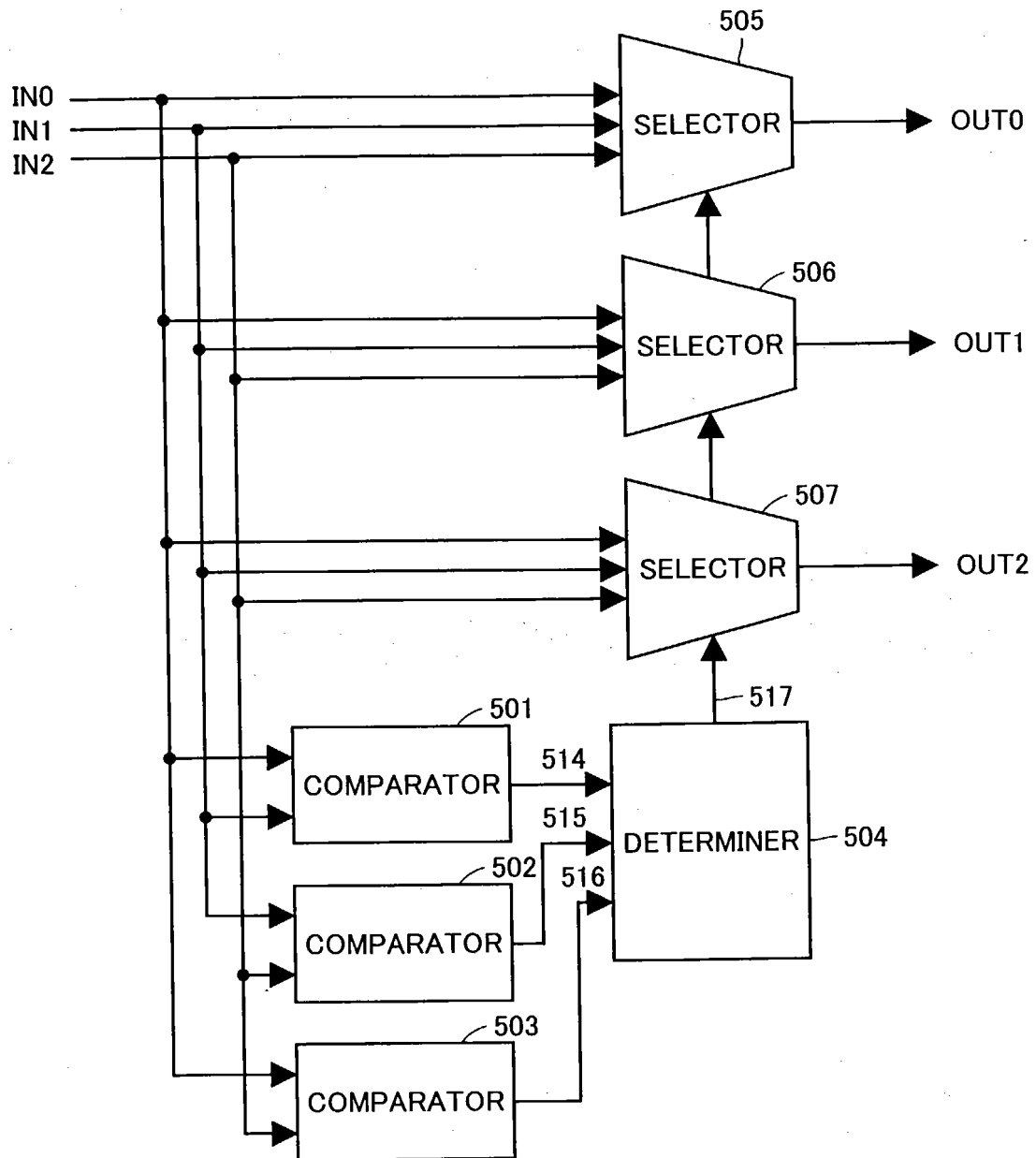


FIG.4

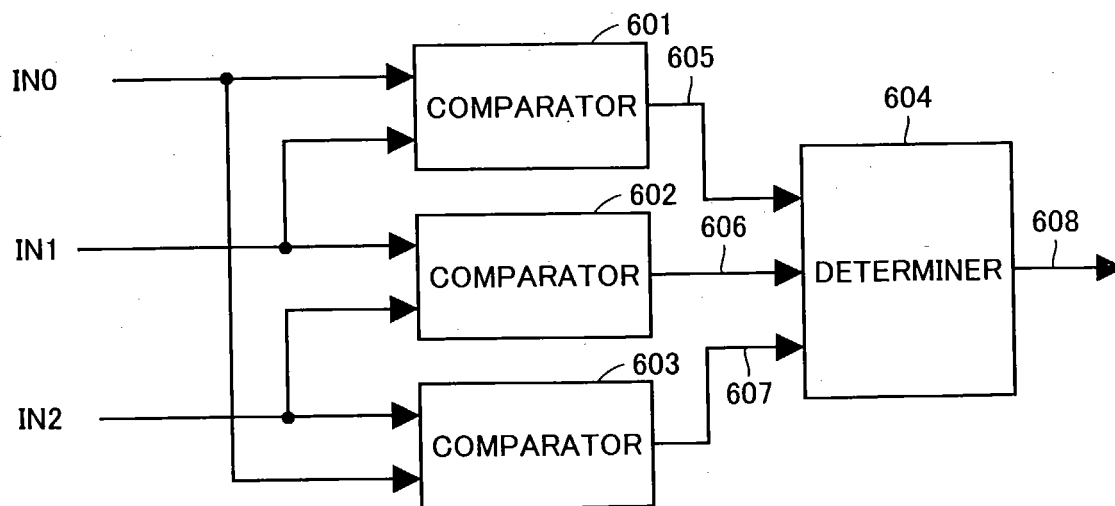


FIG.5

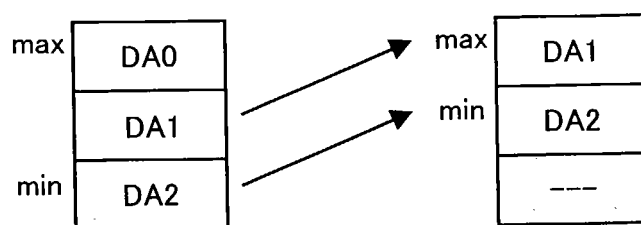


FIG.6A

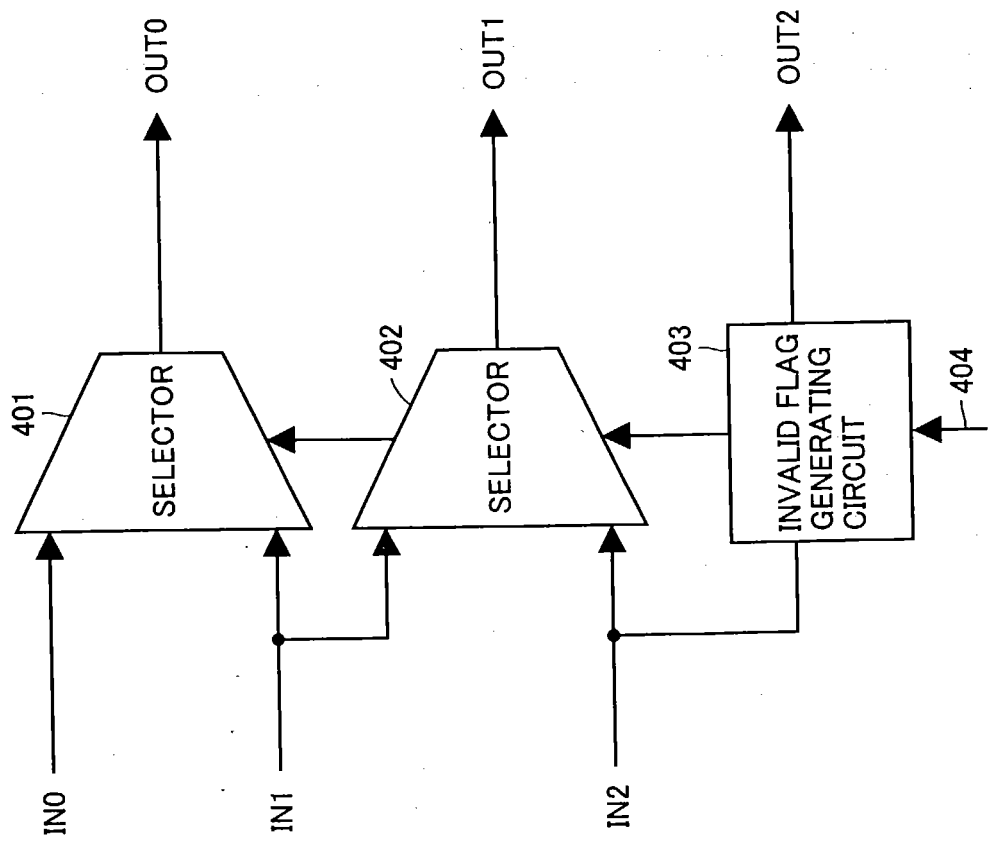


FIG.6B

OUTPUT	404: SHIFT SIGNAL		
	ON	OFF	
OUT0	IN1	IN0	
OUT1	IN2	IN1	
OUT2	INVALID	IN2	

FIG. 7

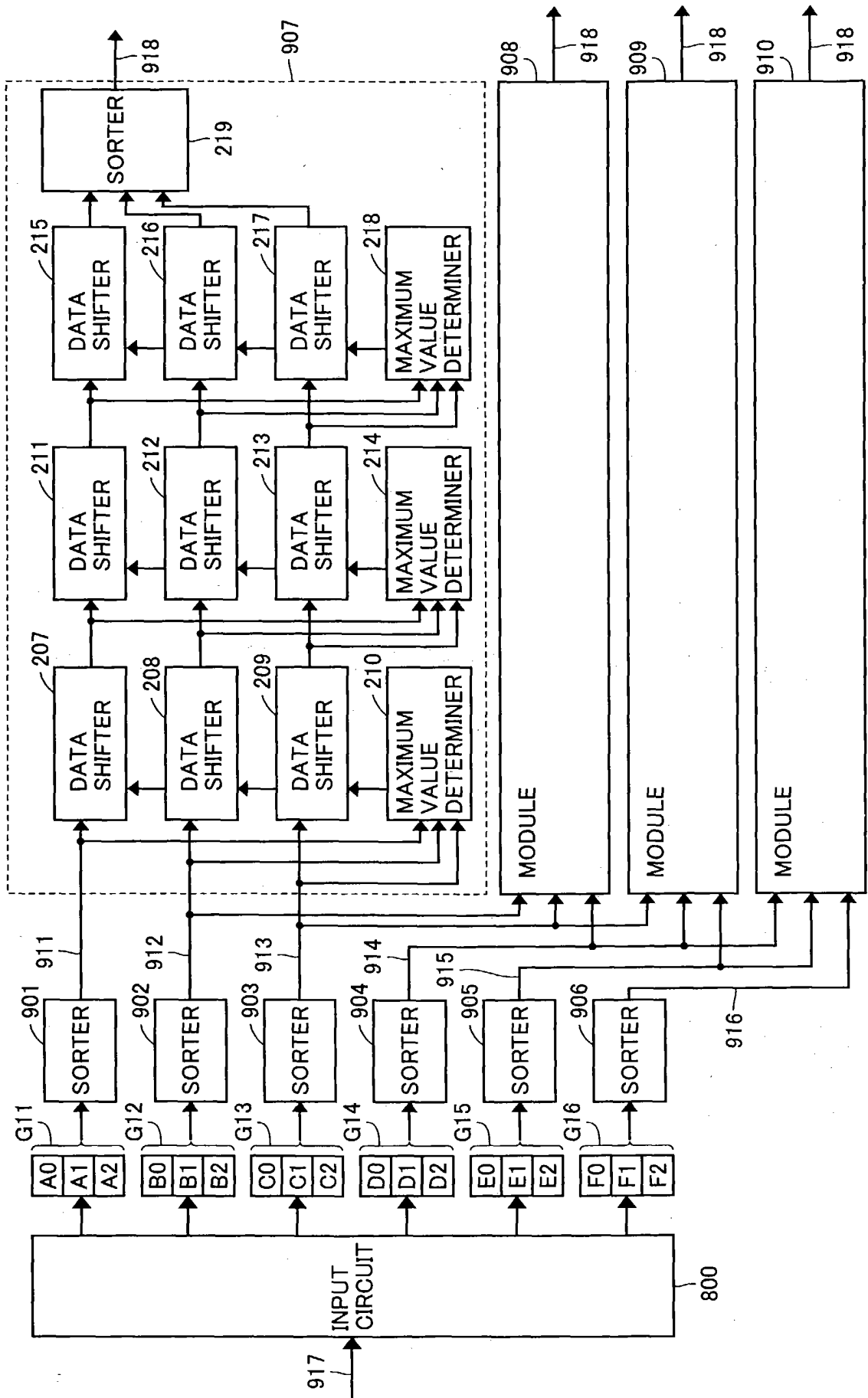


FIG.8

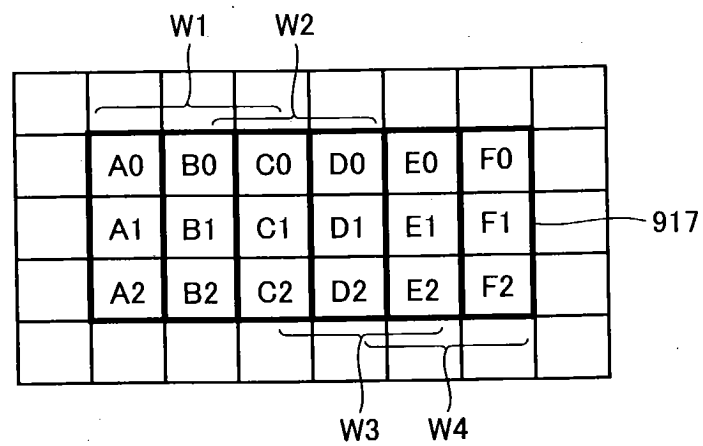


FIG.9

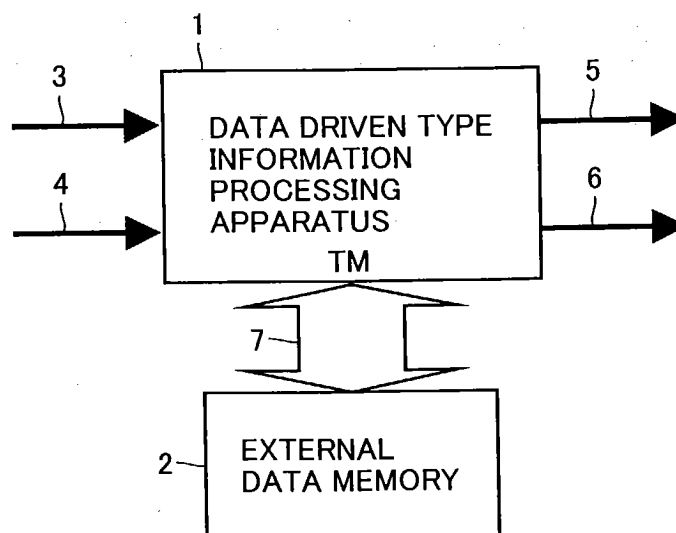


FIG.10

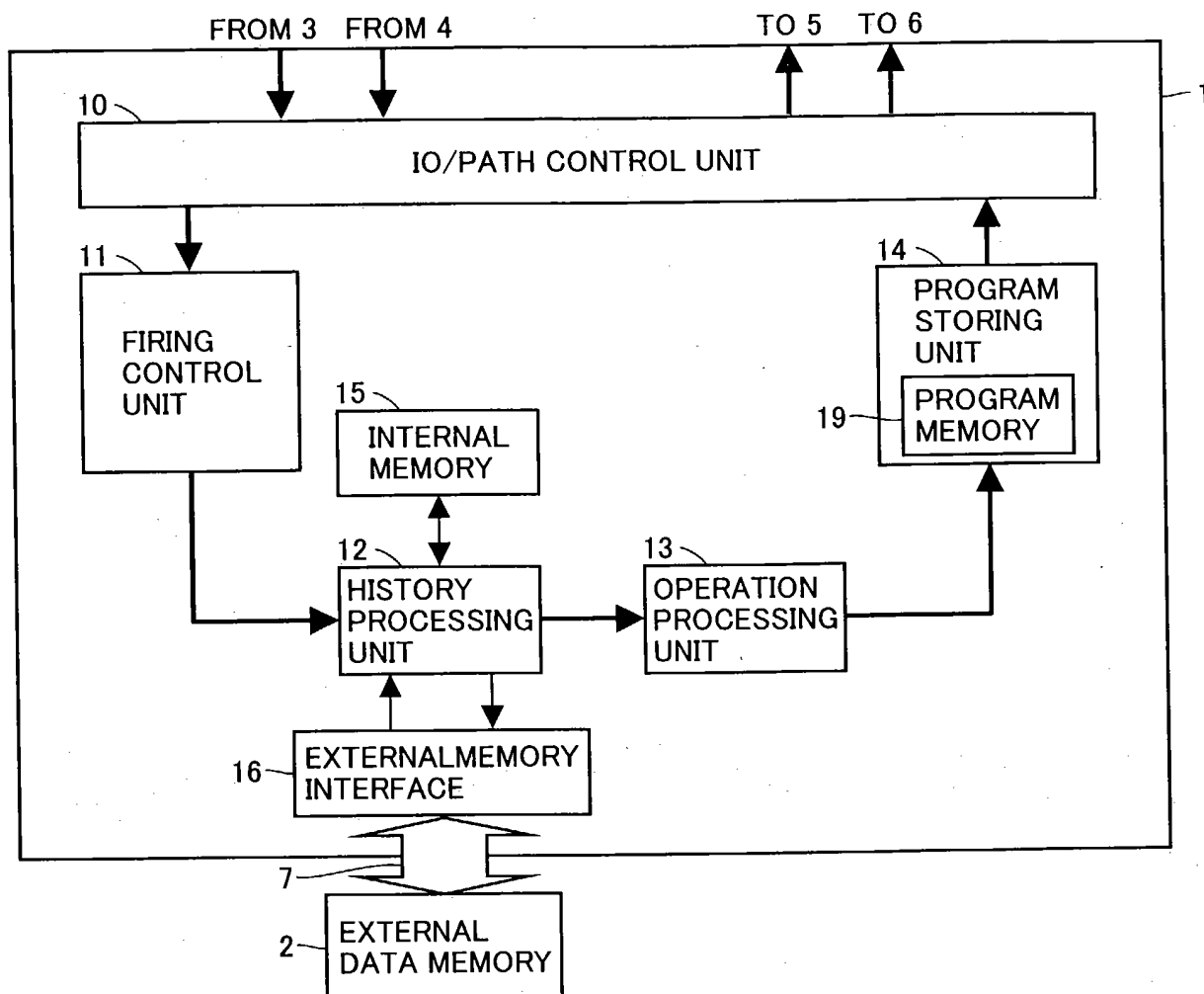
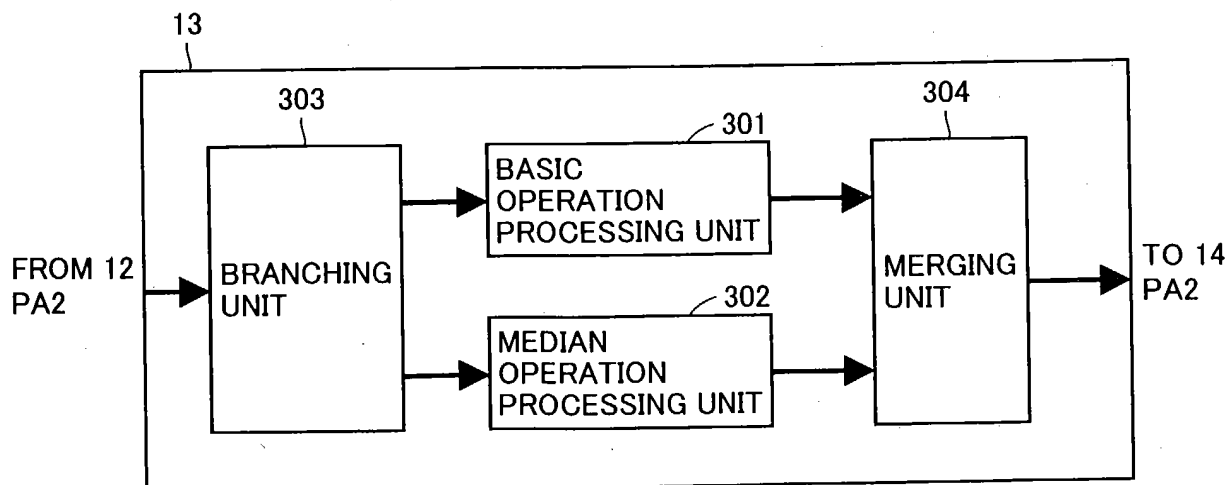


FIG.11





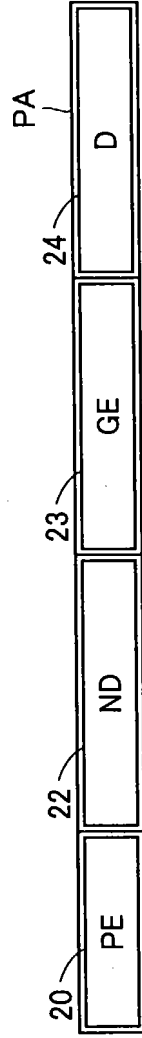


FIG. 12A

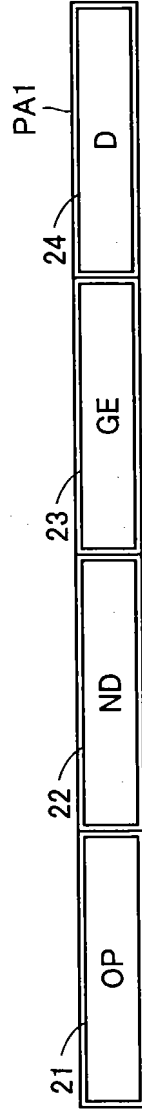


FIG. 12B

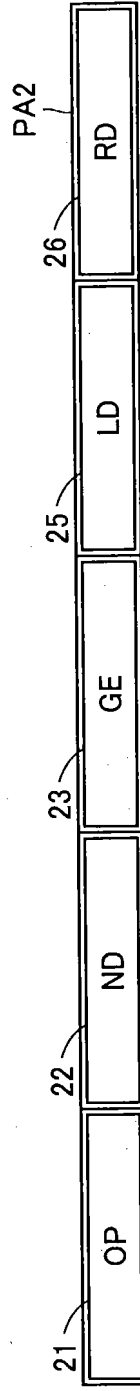


FIG. 12C

FIG.13A

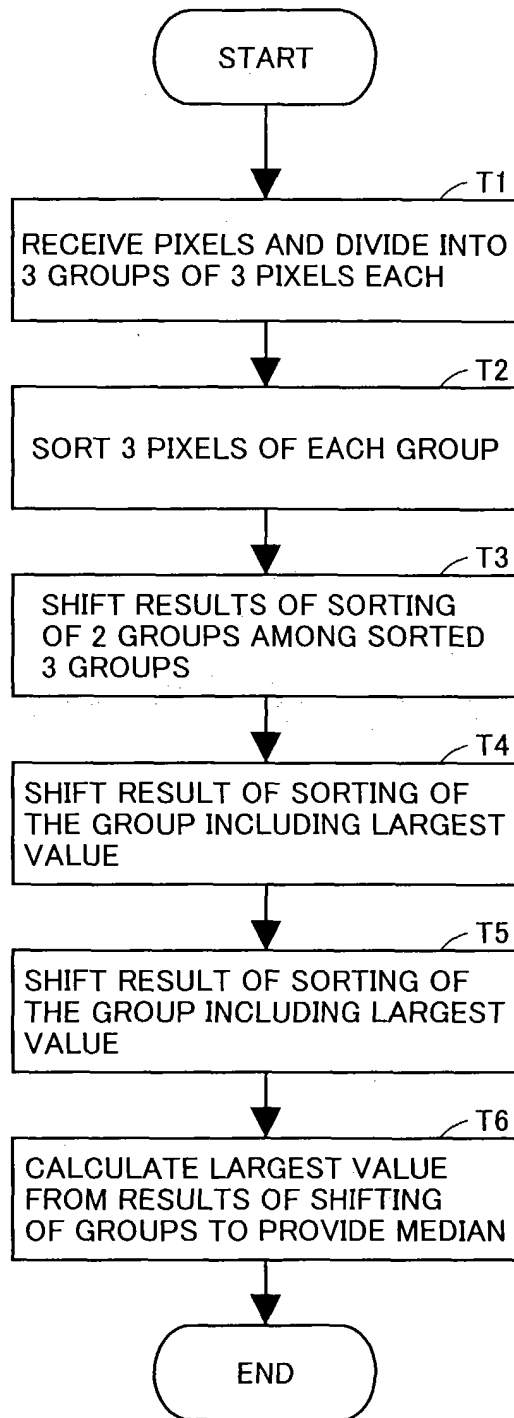


FIG.13B

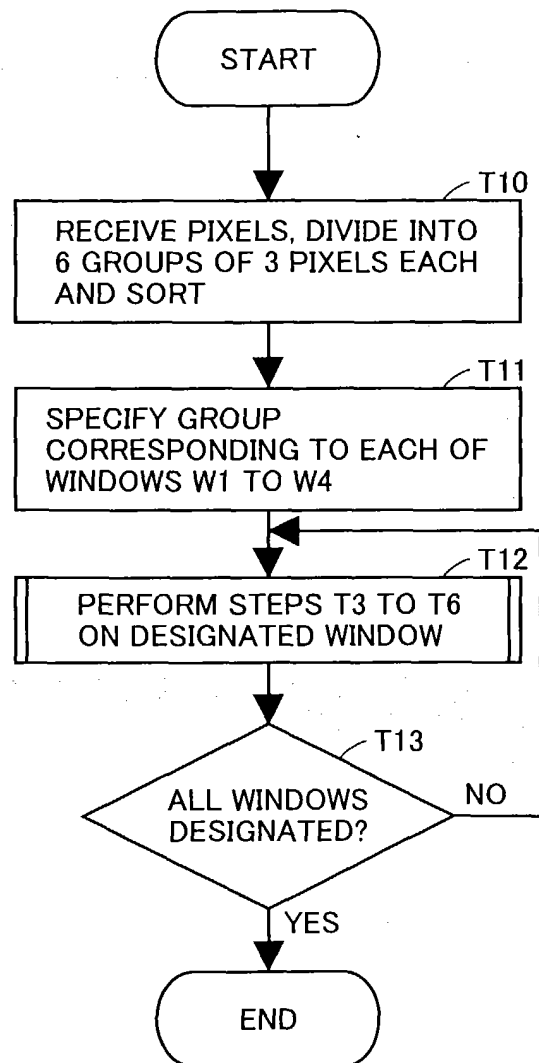


FIG.14

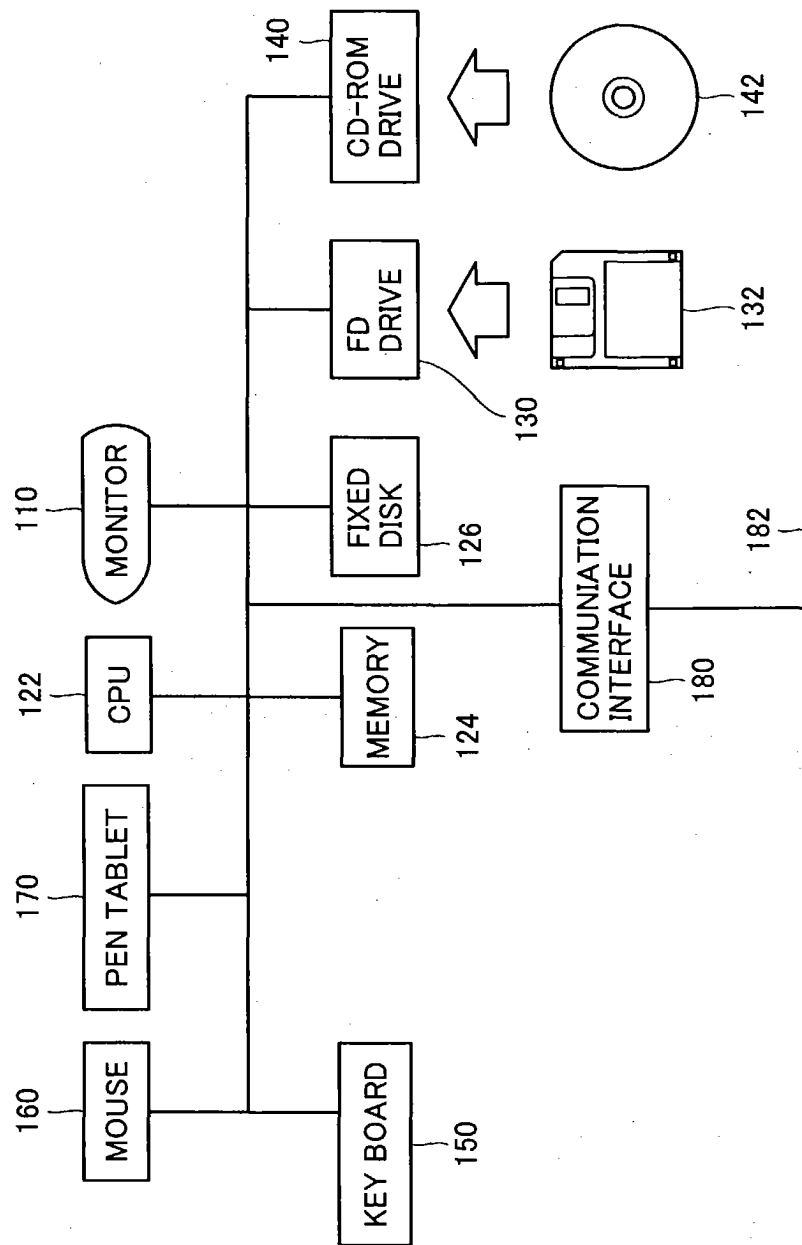


FIG.15 PRIOR ART

